

DFW

Attorney Docket No. 5649-1226

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor: **Kim et al.**

Confirmation No.: **2748**

Serial No.: **10/806,521**

Group Art Unit: **2812**

Filed: **March 23, 2004**

Examiner: **Walter Lee Lindsay, Jr.**

For: **METHOD OF FABRICATING A SEMICONDUCTOR SUBSTRATE FOR
REDUCING WAFER WARPAGE**

Date: June 29, 2005

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT AND RESPONSE TO RESTRICTION REQUIREMENT

Sirs:

Applicants provide the present Amendment to address the issues raised in the Office Action mailed June 9, 2005 (the Action).

It is not believed that an extension of time and/or additional fee(s), including fees for additional claims, are required, beyond those that may otherwise be provided for in documents accompanying this paper. In the event, however, that an extension of time is necessary to allow consideration of this paper, such an extension is hereby petitioned under 37 C.F.R. §1.136(a). Any additional fees believed to be due in connection with this paper may be charged to our Deposit Account No. 50-0220.